

Curriculum Vitae

Sharanbasappa Shetkar

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Summary:

- Organized leader and team player with 12+ years of experience in to Academic profession.
- Expertise in dealing with subjects like, **Field Theory, Signals & Systems, Control Systems, Low Power VLSI Designing, Basic Electronics, Computer Organization, Operating Systems etc.**
- Currently working as **HOD of Electronics & Communication Engineering** department in Lingaraj Appa Engineering College, Bidar
- Has worked as Internal DCS during external VTU Examinations.
- Completed **Ph.D.** in Electronics from Visvesvaraya Technological University, Belagavi.

Educational Credentials:

- **Ph.D.:** “Enhancing the Efficiency of Optical Filtering for Visible Light Communications” in the department of Electronics & Communication from VTU Belagavi.
- **Master of Technology** in Digital Electronics & Communication from VTU Belgaum, Karnataka India

- **Bachelor of Engineering** in the Department of **Electronics & Communication Engineering**, from` Rural Engineering College Bhalki Karnataka, India.

Competencies:

- Organizing workshops and seminars
- Evaluating University Answer Scripts
- Conducting Internals
- Project Guidance

Experience Summary:

Organization	Designation	Duration
Lingaraj Appa Engineering College, Bidar	Assistant Professor	01/08/2012 – 30/09/2022
Lingaraj Appa Engineering College, Bidar	Associate Professor	01/10/2022 – 30/09/2024
Lingaraj Appa Engineering College, Bidar	Professor	01/10/2024 – till date

Professional Experience: 12+ years

Lingaraj Appa Engineering College: (August 2012 - till date)

Responsibilities:

- Conducting theory classes for the subjects allotted.
- Working also as Exam Controller for University Examinations for 2 years.
- Conducting Seminars and Guiding Project work.
- Evaluation of University Answer scripts.

- Laboratories handled: Analog Electronics Lab, Logic Design Lab, HDL Lab, Power Electronics Lab.

Professional Training Programs :

- Organized One Day National Level Seminar on “**DIGITAL IMAGE PROCESSING**” on 6th April 2018 at Lingaraj Appa Engineering College.
- Organized Two Days Workshop on “**Advance Microcontrollers and its Applications AM-APPS**” on 11th and 12th March 2016 in association with VED LABS (Versatile Engineering Designs) at Lingaraj Appa Engineering College.
- Organized Two Days Workshop on “**Recent Trends in VLSI Design RTVD-2014**” on 20th and 21st October 2014 at Lingaraj Appa Engineering College.
- Participated in One-week Faculty Development Program on “**Effective Teaching, Learning and Research Skills towards Global Standards in Higher Education**” from 08/01/2015 to 13/01/2015 at GECW & AIET Kalaburagi.
- Participated in One-week workshop on “**MICRO & NANO ELECTRONICS (MAN) 2013-14**” from 06/08/2013 to 09/08/2013 at Guru Nanak Dev Engineering College, Bidar, Karnataka, India.
- Participated in One-week State Level Faculty Development Program on “**Emerging Trends in Teaching, Learning and Research**” from 04/01/2016 to 09/01/2016 at AIET Kalaburagi, Karnataka.
- Attended 3 days Webinar Series on “**Emerging and Transformative Technologies – Post Pandemic Era**” from 29/06/2020 to 01/07/2020 at AIET Kalaburagi.
- Attended One day International Webinar on “**The Effect of Music on Human Brain**” on 09/07/2020, Organized by Department of ECE, GNDEC, Bidar.
- Attended One day International Webinar on “**Role of User Experience in Engineering**” on 11/07/2020, Organized by Department of ISE & CSE, SDM College of Engineering & Technology, Dharwad.

- Participated in Five days Online Faculty Development Program on **“Contemporary Research Trends in Electronics & Communications”** from 06/07/2020 to 10/07/2020 from VTU-Centre for PG Studies and Regional Office, Kalaburagi.
- Attended TEQIP 1.3 Sponsored One week Faculty Development Program on **“Outcome Based Education and NBA Accreditation”** from 20/01/2020 to 24/01/2020 at Dr. P.G. Halakatti College of Engineering, Vijayapur.

PUBLICATIONS:

- Haralayya, Bhadrappa, and Sharanbasappa Shetkar. "Algorithmic Trading and Market Liquidity: A Comprehensive Analysis of High-Frequency Trading Strategies." 2024 First International Conference on Technological Innovations and Advance Computing (TIACOMP). IEEE, 2024.
- Sharanbasappa Shetkar, Baswaraj Gadgay , Shubhangi D. C. “Advancements in Face Recognition Using Deep Learning Techniques: A Comprehensive Review” Journal of Harbin Engineering University. Volume 45 ,Issue 3 Pages 566-571.
- Shetkar, Sharanbasappa, Baswaraj Gadgay, and D. C. Shubhangi. "Machine Learning Based Adaptive Filtering Of Mixed Noises In Visible Light Communication." SJIS-P 35.3 (2023): 702-709.
- Shetkar, Sharanbasappa. "Applications and Issues in Visual Light Communications." Journal of Optoelectronics and Communication 6.2.
- Shetkar, Sharanbasappa, Basawaraj Gadgay, and D. C. Shubhangi. "A NOVEL MIMO-NOMA BASED VLC TRANSCIEVER DESIGN FOR HIGH BER VS. SNR PERFORMANCE." JOURNAL OF HARBIN INSTITUTE OF TECHNOLOGY 53.12 (2021): 2021.
- Shetkar, Sharanbasappa, and Baswaraj Gadgay. "A Sallen Key All Pass Filter for Noise Reduction in Visible Light Communication Systems."
- Bachanna, Prashant, Vivek Jalad, and Sharanbasappa Shetkar. "Design and analysis of high speed low power reusable on chip bus based on wishbone." 2014 Fifth International Conference on Signal and Image Processing. IEEE, 2014.

- Sharanbasappa Shetkar, Baswaraj Gadgay, Shubhangi D C, “Adaptive Filtering of Multiple Noise Sources based on Convolutional Neural Network based Machine Learning Algorithms in Visible Light Communications”, Journal of Optoelectronics Laser, December 2021.
- Sharanbasappa Shetkar, Baswaraj Gadgay, “A Variant MIMO-OFDM VLC (VMOV) Optimization model for WiMAX Network”, 9th International Conference on Innovations in Electronics & Communication Engineering (ICIECE - 2021), August 13 – 14, 2021.

PATENTS:

- Dr P Munaswamy, Sharanbasappa Shetkar, Sangeetha M K, Dr Shashikala Parameshwarappa, “AI-BASED ONLINE ADMISSION SCREENING SYSTEM”, published in National Indian Patent, Application Number: IN202441001839, 09th February 2024.
- Baswaraj Gadgay, Sharanbasappa Shetkar, “Enhancing the Efficiency of Optical Filtering for Visible Light Communications”, published in National Indian Patent, Application Number: IN202141032907, 30th June 2021.

BOOKS PUBLISHED:

- Sharanbasappa Shetkar, Baswaraj Gadgay, “Enhancing the Efficiency of Optical Filtering for VLC, published by LAP Lambert Academic Publishing, ISBN-13: 978-620-5-64100-2, ISBN-10: 6205641003, EAN: 9786205641002, Number of pages: 136, Published on: 2023-01-24.

PROFESSIONAL BODIES/CHAPTERS MEMBERSHIPS:

- ISTE – LM130727 (Life Member)
- IETE – M502187 (Life Member)
- IEEE - 97705216

ACADEMIC IDENTITY:

- **Scopus** : 56126619900
- **Google Scholar** : <https://scholar.google.com/citations?user=65NBkdYAAAAJ&hl=en>
- **ORCHID**: 0000-0003-1748-6919
- **Research Gate** : <https://www.researchgate.net/profile/Sharanbasappa-Shetkar>

Personal Details:

- **Date of Birth:** 11-DEC-1986
- **Marital Status:** Married
- **Address:** H.NO:8-6-41, J.P. Nagar,
KHB Colony,
Bidar-585401, Karnataka, India.

I hereby declare that the above information stated are true and correct to the best of my knowledge and belief.