Sagar Buyya

Phone: 09844774387
✉ : svmbuyya@gmail.com

Career Objective:

      Self-directed, enthusiastic educator with a passionate commitment to student development and the learning experience. Skilled in the design of challenging, enriching and innovative activities that address the diverse interests and needs of students.

Educational Qualification:

Examination	Institution	University/Board	Year of Passing	Percentage
(PhD) (Machine Learning)	V.T.U RRC Belagavi	V.T.U	Submitted the Thesis (2024)	Waiting for external Viva
M.Tech. in Digital Communication Systems	M.S.Ramaiah Institute of Technology. Bangalore	V.T.U	2011	8.60(CGPA) (78%)
B.E.,ECE	Guru Nanak Dev Engineering Collage Bidar.	V.T.U	2008	56%
XII	National Collage Bidar.	P.U.Board	2002	63.5%
X	Vidyaranya High School, Bidar.	K.S.E.E Board	2000	79.84%

Career Summary:

- Worked as Soft-ware engineer at VeriFone India (10 months).

- A progressive and multitalented lecturer with extensive practical knowledge having Result oriented teaching methodologies since August 2012 to till date.

- Excellent knowledge of electronics technology and the new methodologies.

- Proficient in managing the opportunities for students.

- Excellent in providing the real time knowledge about technology.

- Excellent knowledge of subject and also have great practical knowledge

- Managing student learning progression.

- Excellent communication skill to deliver and present learning effectively.

Paper Publications:

All Scopus Publications
A research article title "On Realizing Costs from Shift in Security Values with Shift of Time Frames Using Virtual Currencies" Published in International Journal of Advance Research in Engineering and Technology (IJARET). Volume 11,Issue 7, July 2020 published by Mr. Saga Buyya, Dr,Baswaraj Gadgay, Dr. Shubhangi Digamber Chikte with DOI:http://doi.org/10.17605/OSF.IO/AUP92.

A research article title "To Reduce The Error Proportion in Predicting For the Future Stock Prices" A ES Volume: 54, Issue: 02, December 2022 published by Mr. Sagar Buyya Buyya, Dr, Baswaraj Gadgay, Dr. Shubhangi Digamber Chikte with ISSN:2096-3246

A research article title "Stock Market Prediction Method Obtains Accurate Stock Prediction Prices and Measures Performance", published in Harbin Gongye Daxue Xuebao/Journal of Harbin Institute of Technology. Volume 54, Issue 5, 2022, published by Mr. Sagar Buyya, Dr, Baswaraj Gadgay, Dr. Shubhangi Digamber Chikte with ISSN: 0367-6234

A research article title "The Cost Realization on Time Frame Shift of Securities" Volume: 44 No. 3(2023), 2023 published in Tuijin Jishu/Journal of Populsion Technology. Published by Mr. Sagar kumar Buyya, Dr, Baswaraj Gadgay, Dr. Shubhangi Digamber Chikte with ISSN: 1001-4055.

Faculty Development Programs attended:

- 1 Day FDP on NEP 2020: Touching ICEBURG 6th Jan 2023 at Lingaraj Appa Engineering College.

- VTU HUMAN RESOURCE DEVELOPMENT CELL (VTU-HRDC)-Effective Teaching Techniques in Digital Signal Processing 26th to 30th August-2019. Muddenhalli Chikkaballapur.

- 3 days FDP on Embedded system design using ARM CORTEX M3 29th to 31st January 2018.

- One week State level FDP on Emerging Trends in Teaching, Learning and Research. 4th to 9th Jan 2016.

- One week State level FDP on Effective Teaching Learning and Research Skills towards Global Standards in Higher Education. 8th to 13th Jan 2015

- Department of PG studies, Regional Office, Kalburgi- Role of Teachers: Challenges and Opportunities in Technology 7-May-2014 to 10-5-2014.

- One week FDP from 15th Jan to 24th Jan -2013 at Appa Institute of Engineering and Technology Kalburgi.

- State level one week program on Effective teaching, learning and research skills towards global standards.19th to 24th July 2016. Organized by AIET.

Details of memberships in Professional societies:

- Member, Life time member of Indian Society for Technical Education. LM- 130730
- Member, The Institution of Electronics and Telecommunication Engineering.F-502184
- IEEE Graduate Student Member

Key Responsibilities and skill sets:

- Excellent Presentation, motivational and leadership skills.

- Strong analytical, logical and mathematical skills.

- Ability to handle the students.

- Ability to provide quality knowledge.

- Participated in student recruitment, registration, and placement activities.

- Participating in campus and community events

- Helping the students for completing their project works

- Preparing teaching material and utilizing it for the progress of students.

- Initiate, facilitate, and moderate classroom discussions.

Computer proficiency:

- Operating Systems   :   Windows XP, 7.

- Language Known   :   Basic concepts of C.

- Tools   :   Mat lab, Xilinx.

- Assembly Languages   :   Microprocessor 8086.

Area of Interest:

- Signals and System.
- Digital Signal Processing
- Real-Time Operating System
- Microprocessor 8086.
- Wireless Communications
- AI
- Machine Learning
- Python.

Date of Birth   :   25.12.1984.

Father's Name   :   Sagar Buyya

Nationality   :   Indian.

Languages Known   :   English, Hindi, and Kannada

Permanent Address   :   #8-11-569, Sharan Nagar, K.E.B Road, Near National Collage

                                            Gate. Dist. Bidar, State Karnataka.

Declaration:

        I hereby declare that all the above furnished information is true to the best of my knowledge.

Date:

Place: **Sagar Buyya**